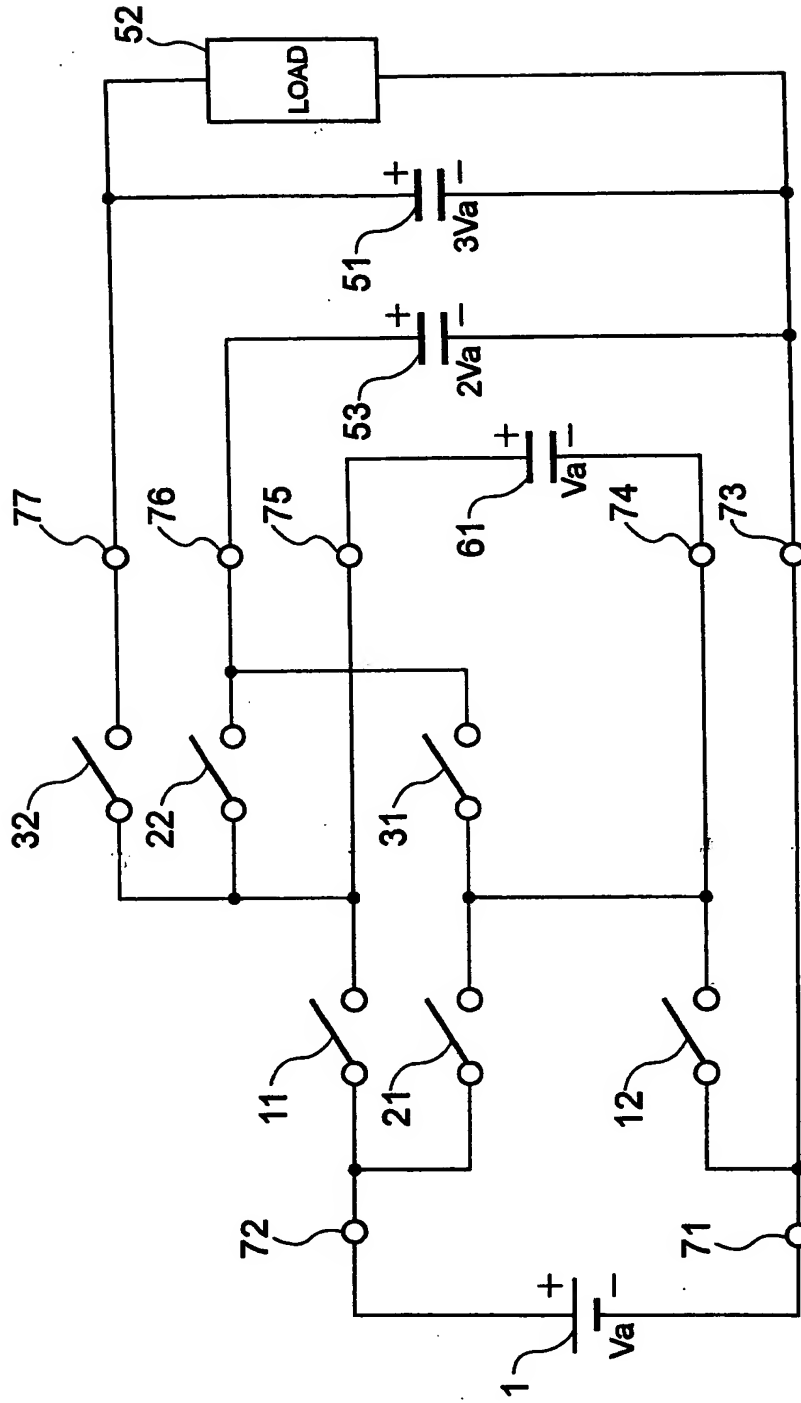
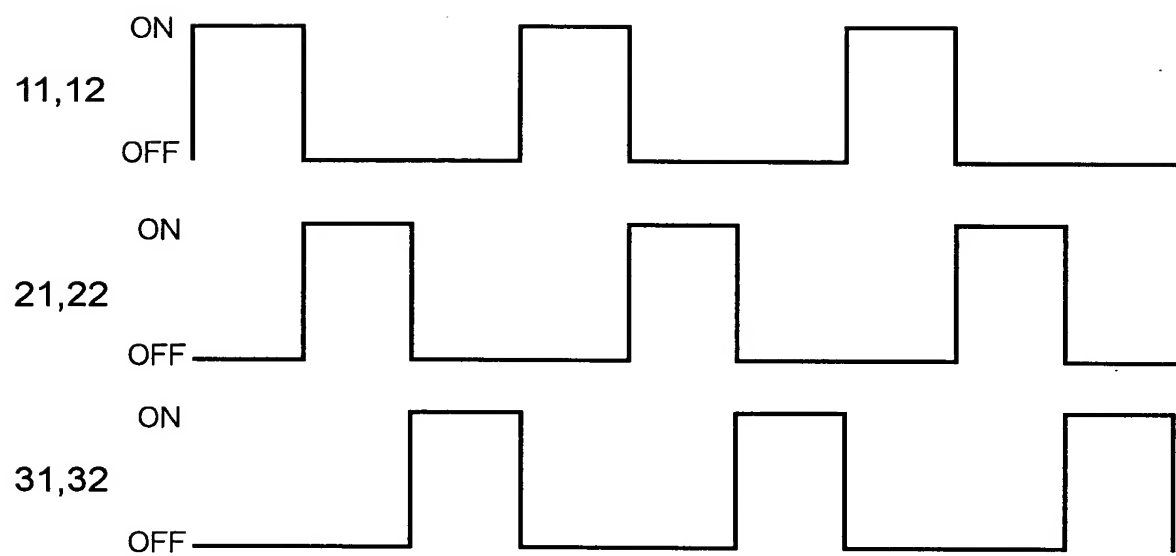


FIG. 1



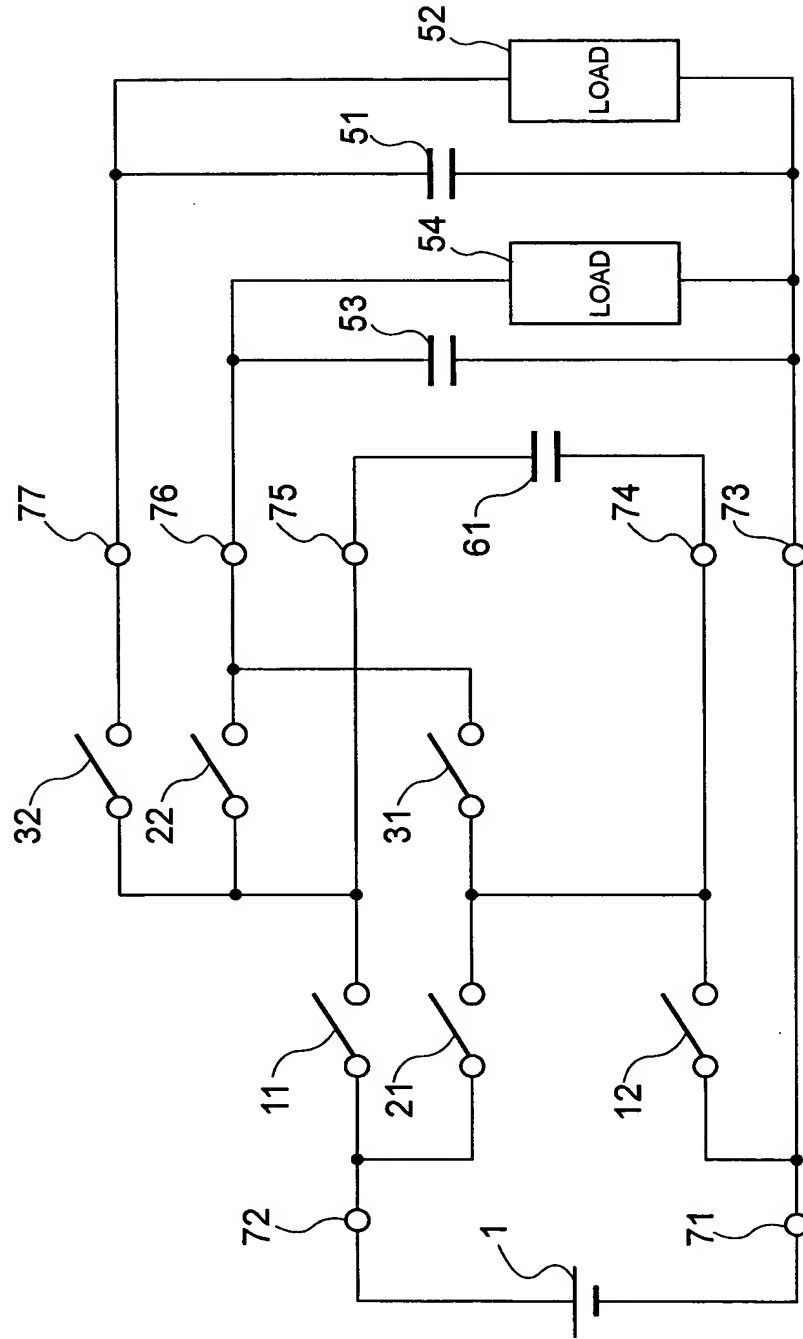
TRIPLE BOOSTER CIRCUIT OF PRESENT INVENTION

FIG. 2

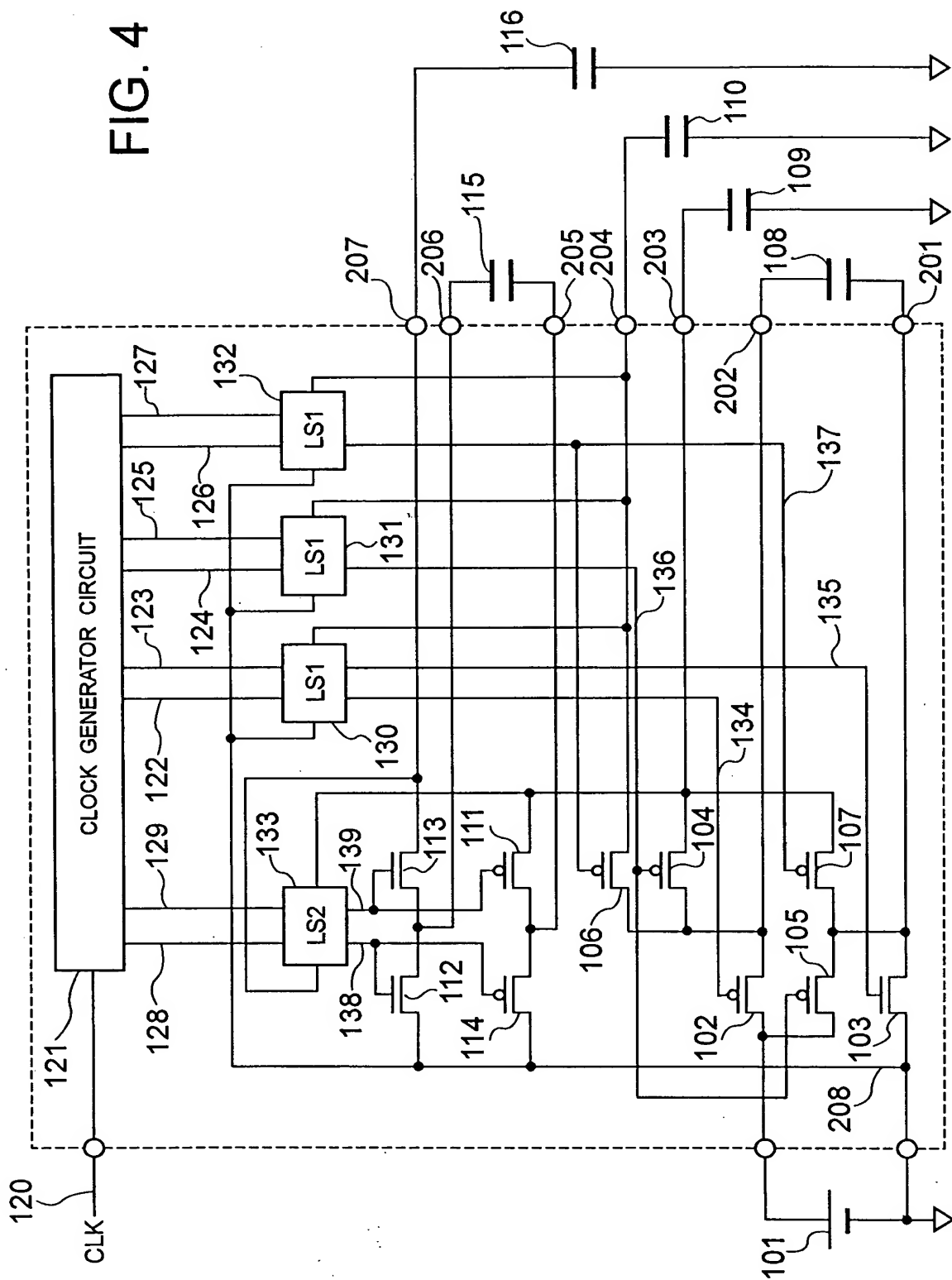


TRIPLE BOOSTING CONTROL SIGNAL OF PRESENT INVENTION

FIG. 3

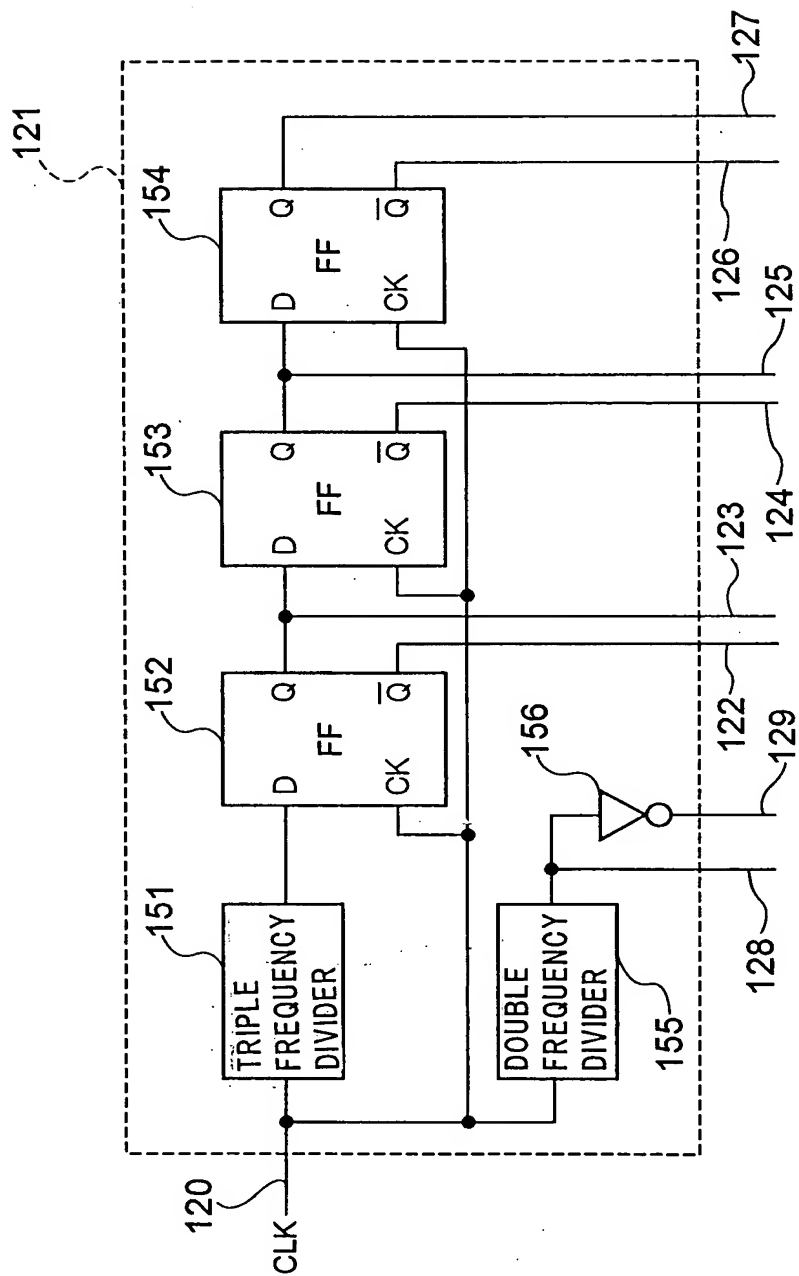


DOUBLE AND TRIPLE BOOSTER CIRCUIT OF PRESENT INVENTION



POWER SOURCE CIRCUIT FOR DISPLAY DEVICE

FIG. 5



CLOCK GENERATOR CIRCUIT

FIG. 6

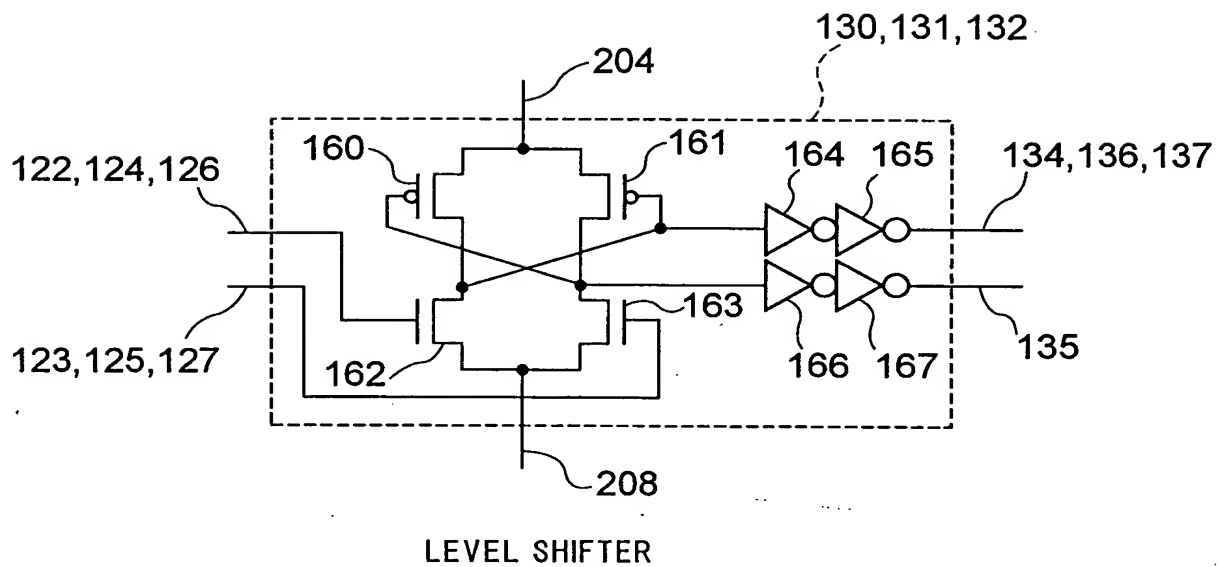


FIG. 7

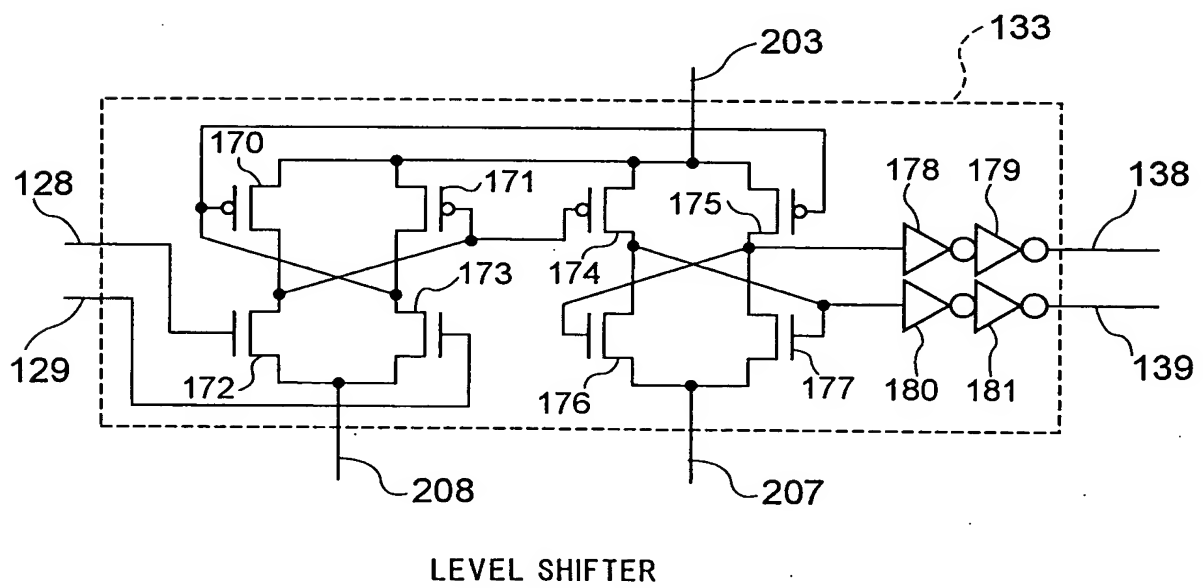
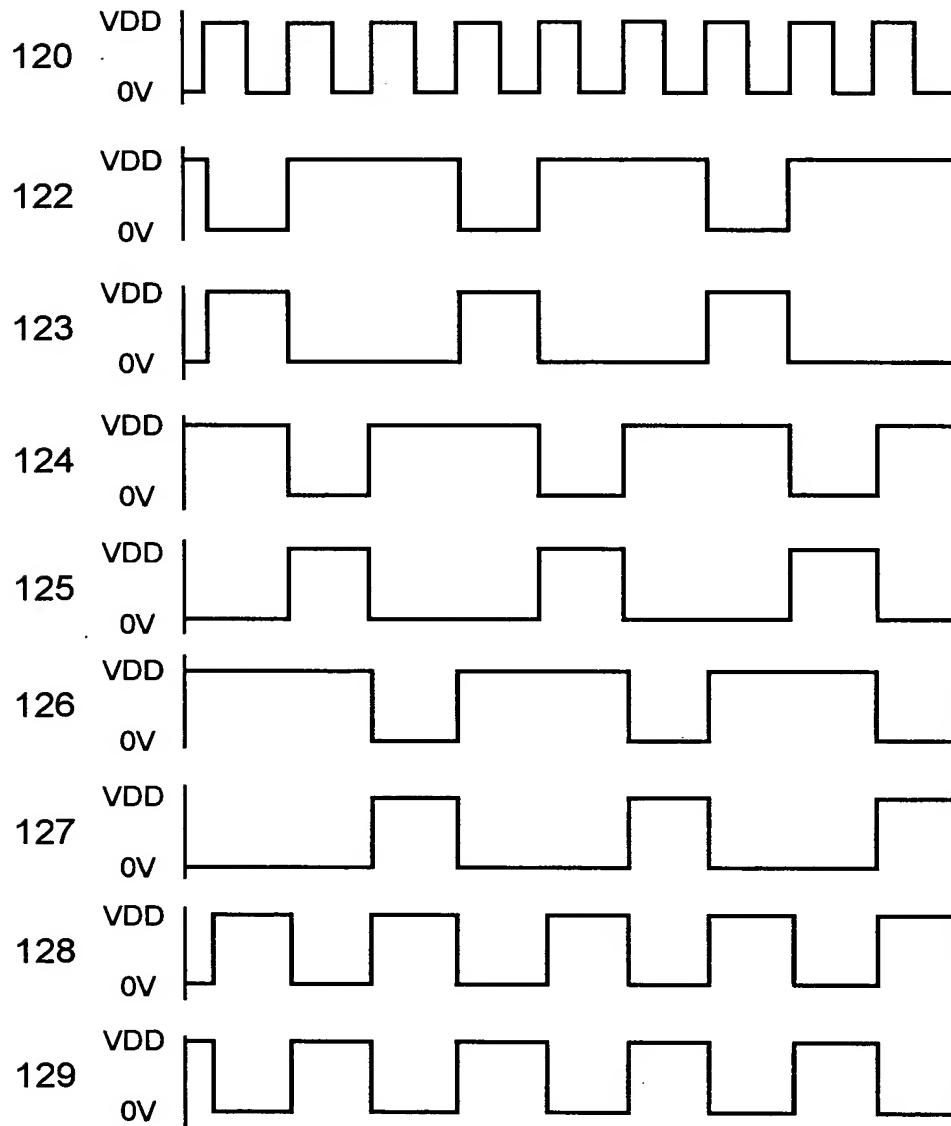


FIG. 8



SWITCH CONTROL SIGNAL

FIG. 9

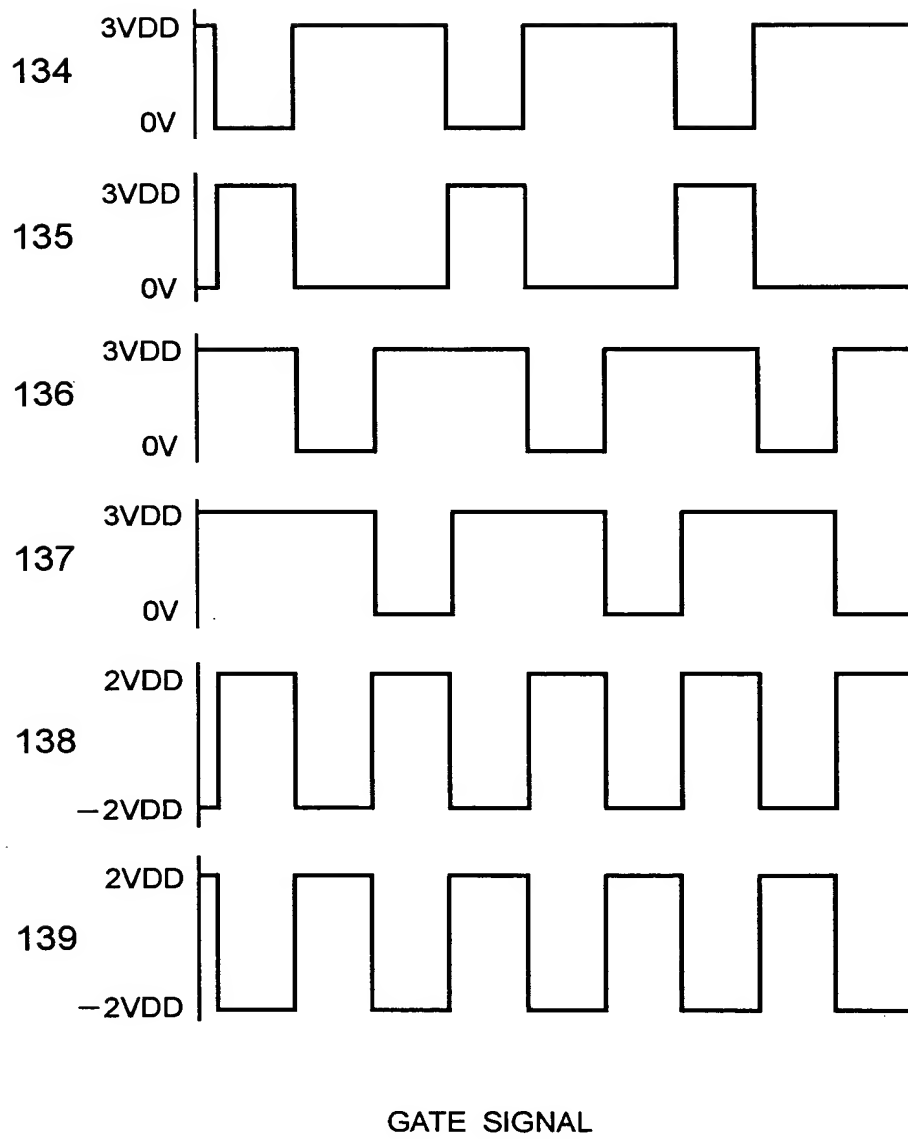
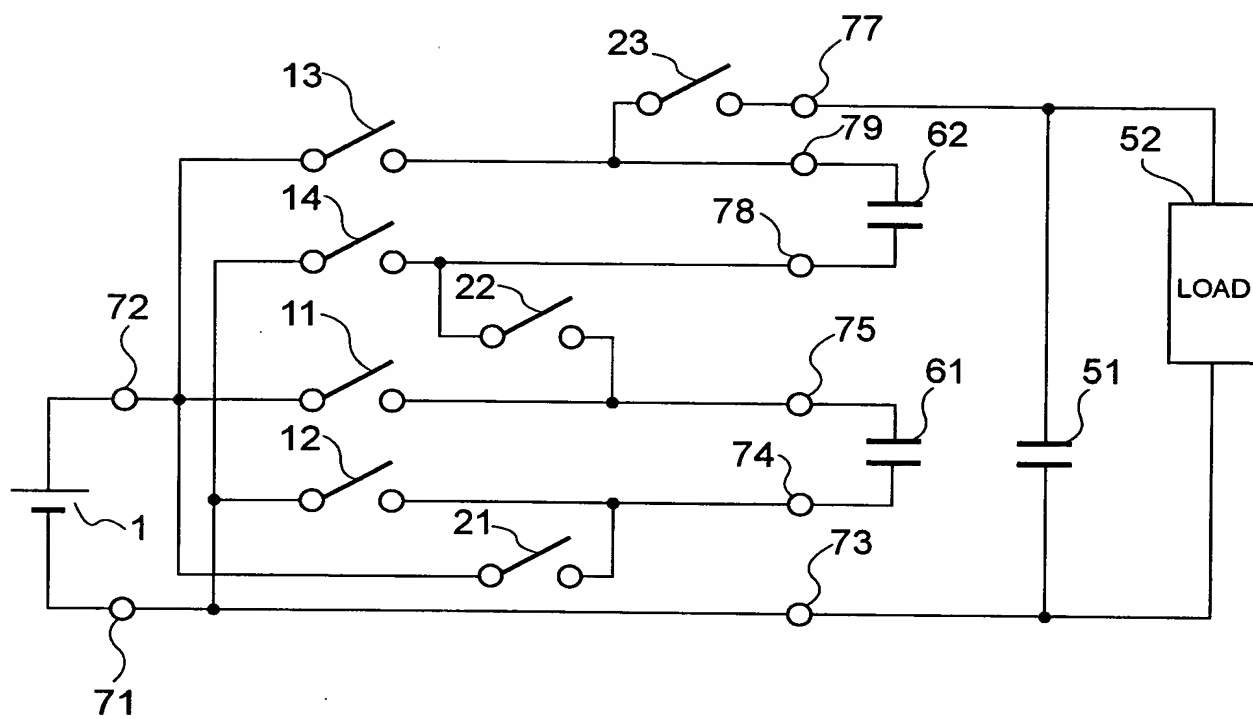
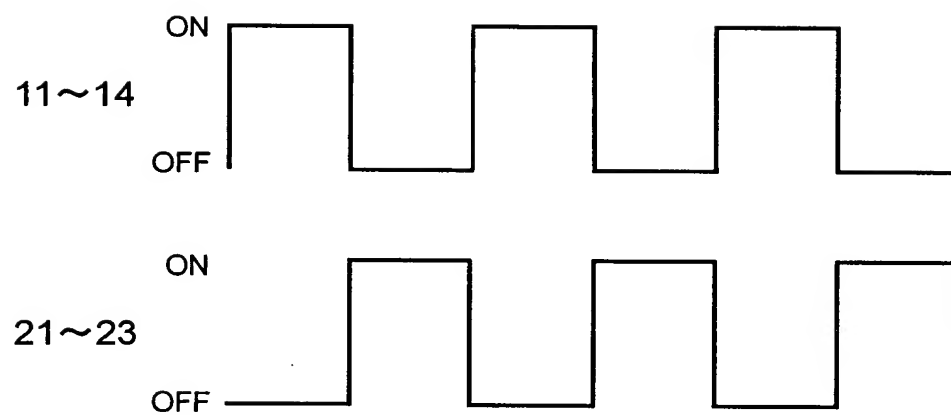


FIG. 10



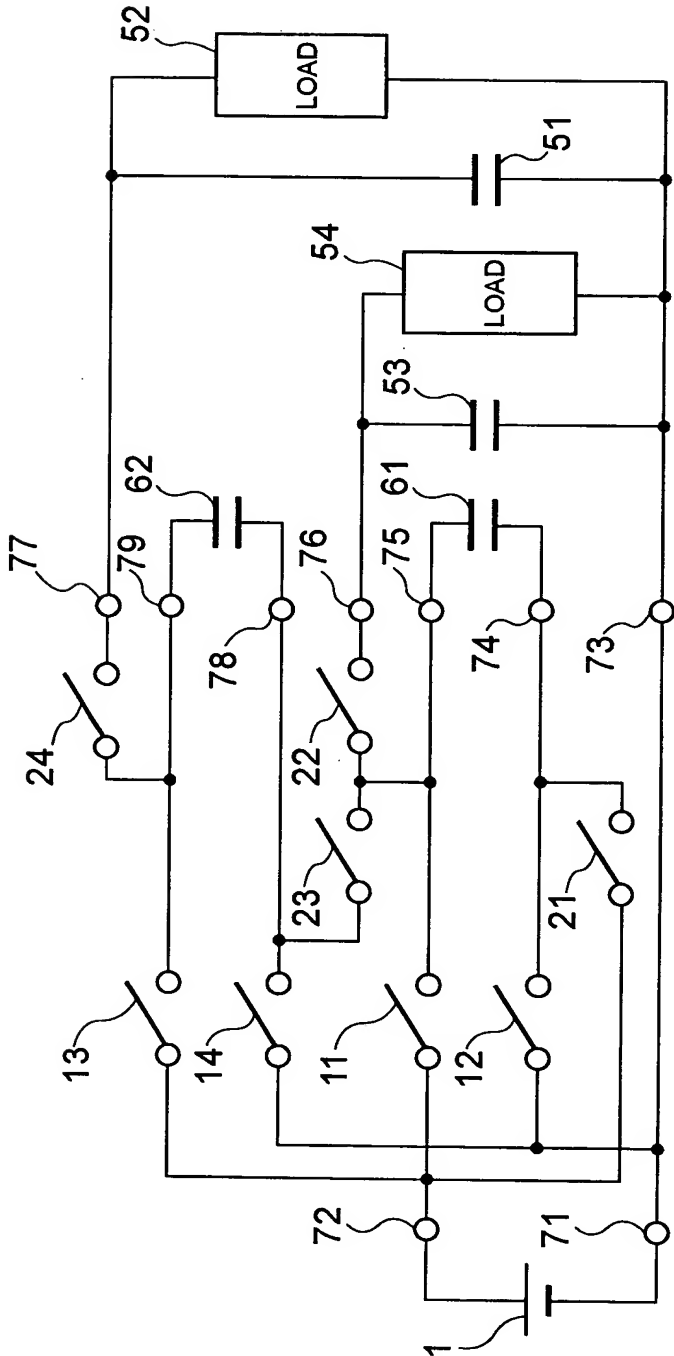
CONVENTIONAL TRIPLE BOOSTER CIRCUIT

FIG. 11



CONVENTIONAL TRIPLE BOOSTING
CONTROL SIGNAL

FIG. 12



CONVENTIONAL DOUBLE AND TRIPLE BOOSTER CIRCUIT